

**In the Specification:**

On page 9, after line 15, insert the following:

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MOSFET M103 is alternately enabled when the signal pixel is being read in each row on a column by column basis. MOSFWT M103 is thus simply a switch to allow M104 to function as a current source for the source follower formed by drive transistor M1, cascode transistor M4, switch transistor M103 and current source M104. MOSFWT M105 is asserted during the reset interval to discharge the photogenerated signal in the entire imaging sensor to the voltage set by  $V_{Drain}$ .  $V_{Drain}$  is also the supply voltage for the inverting reset amplifier formed by transistors M1, M4, M101 and M102 to reset the snapshot image formed at the gate of M1 via the negative feedback associated with tapered reset. So, the path leg through M101 is used for tapered reset of the snapshot image, path leg through M103 is used for readout of the snapshot image stored at the gate of M1, and path leg through M105 is used for photodiode reset.

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